

Notice of References Cited			Application/Control No.	Applicant(s)/Patent Under Reexamination	
			10/694,302	LARSON ET AL.	
Examiner		Art Unit		Lev I. Iwashko	2186
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
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FOREIGN PATENT DOCUMENTS

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	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Chavarria-Miranda, Daniel and John Mellor-Crummey, "An Evaluation of Data-Parallel Compiler Support for Line-Sweep Applications". April 2003, Journal of Instruction-Level Parallelism 5, pp. 1-29.
	V	Cray T3ETM Fortran Optimization Guide - 004-2518-002, "Chapter 4. Single-PE Optimization - Section 4.3: Optimization for Cache", January 1999, Sections 4.3.0-4.3.3.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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